

# Report Requirement

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## ◆ Formal Report Format:

- Including the lab introduction, description, the detailed design of your cache simulator, results, discussion, and conclusion.
- Details on your input parameters and output results.

## ◆ Results:

- Show your all results in tables and also draw figures according to them (Four configurations for five traces)
  - Number of cache access
  - Number of cache load
  - Number of cache store
  - Average cache hit rate
  - cache hit rate for loads
  - cache hit rate for stores

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## ◆ Trace file format:

- **s 0x1ffff50**
  - s or l means store or load
  - 0x1ffff50 is the 32-bit address in hexadecimal
- **If you simulate the cache latency, there is another format of the trace file including the cycles between two instructions. For example, 6 means the instruction will execute 6 cycles after the completion of the previous instruction.**

## ◆ Upload your codes and report to the FTP:

- **<ftp://172.25.46.144> user: costu2 pass: costu2**
- **File name format: studentID-lastname-firstname.pdf (Report), studentID-lastname-firstname.zip (code).**

## ◆ Due on 11:59 pm, November 11th (Firm Deadline, No Extension).