

# Configuration

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## ◆ Four Configurations

- 1. Cache size: 256KB, Cacheline size: 8 Byte, Direct-mapped

- 2. Cache size: 64KB, Cacheline size: 32 Byte, 4-way set-associative, LRU

- 3. Cache size: 64KB, Cacheline size: 32 Byte, 4-way set-associative, Random

- 4. Cache size: 8KB, Cacheline size: 64 Byte, Fully-associative, LRU

◆ If you simulate the cache latency, the miss penalty is 100 cycle.